

### **Listing and Amendments to the Claims**

This listing of claims will replace all prior versions, and listings, of claims in the application.

Claims 1 – 14 (cancelled)

15. (Currently amended) A method for arbitrarily selectable scaling of input images represented by pixels or subpixels arranged line by line and column by column, the method comprising the steps of:

distributing a number of support points, corresponding to a number of pixels or subpixels in the output image, across the lines or columns of the input image at integer pixel or subpixel distances having a minimum variation from one another, wherein the ratio of the number of support points to the number of pixels or subpixels in a line or column of the input image correspond to the desired scaling factor; and

selecting or calculating a pixel or subpixel value for a pixel or subpixel in the output image from pixel or subpixel values in the input image lying between a corresponding support point and a neighbouring support point;

wherein the method further comprises:

distributing the support points of two successive lines or columns such that the support points of one line or column have an offset with respect to the other line or column, for improving reproduction of fine details in the output image.

16. (Previously presented) The method of claim 15, further comprising the step of determining the values for neighbouring pixels in the output image from the pixels between a corresponding support point and a neighbouring support point such that they have a maximum difference.

17. (Previously presented) The method as claimed in claim 15, further comprising calculating a pixel or subpixel value for a pixel or subpixel in the output image from pixel or subpixel values in the input image lying between a corresponding support point and both neighbouring support points.

18. (Currently amended) A scaling circuit for the arbitrarily selectable scaling of images represented by pixels or subpixels arranged line by line and column by column, having a microprocessor, a program memory and a main memory, and also input means for scaling ratios, wherein the circuit is adapted to execute a method as claimed in ~~one of~~ ~~claims 15 to 17~~.

19. (Currently amended) A film scanner with a drive for a control monitor, which is configured to execute the method of ~~one of~~ ~~claims 15 to 17~~.

20. (Previously presented) A film scanner with a drive for a control monitor, which is configured to include a scaling circuit of claim 18.